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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*  
 This application is a DIV of 09/820,716 03/30/2001 PAT 6,701,396 *OK Cet*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *OK 24*  
 JAPAN 2000-181521 06/16/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 7	TOTAL CLAIMS 2	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>OK</i>				
Verified and Acknowledged	Examiner's Signature	Initials		

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TITLE  
 Data burst transfer circuit, parallel-serial and serial-parallel conversion circuits, and an oscillation circuit

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